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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/649,737	08/28/2003	Hisayuki Kato	67161-088	67161-088 5698	
7590 06/09/2004			EXAMINER		
McDermott, Will & Emery 600 13th Street, N.W.			PRENTY,	PRENTY, MARK V	
Washington, DC 20005-3096			ART UNIT	PAPER NUMBER	
<i>5</i> ,	<b>,</b>		2822		
			DATE MAILED: 06/09/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/649,737	KATO, HISAYUKI				
Office Action Summary	Examiner	Art Unit				
	MARK V PRENTY	2822				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespond nc address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	rely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 28 Au	ugust 2003.					
2a) This action is <b>FINAL</b> . 2b) ⊠ This	action is non-final.					
3) Since this application is in condition for allowar closed in accordance with the practice under E	•					
Disposition of Claims						
4) ☐ Claim(s) 1-11 is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1 and 9-11 is/are rejected. 7) ☐ Claim(s) 2-8 is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	vn from consideration.					
Application Papers						
9)☐ The specification is objected to by the Examine	r.					
10)⊠ The drawing(s) filed on <u>28 August 2003</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
Applicant may not request that any objection to the	-	<b>,</b> ,				
Replacement drawing sheet(s) including the correcting 11) The oath or declaration is objected to by the Ex		• ,				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) □ All b) □ Some * c) □ None of:  1. □ Certified copies of the priority documents 2. □ Certified copies of the priority documents 3. □ Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Application ity documents have been receive i (PCT Rule 17.2(a)).	on No d in this National Stage				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summary					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>August 28, 2003</u> .	Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	te atent Application (PTO-152)				

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This Office Action is in response to the papers filed on August 28, 2003.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Stamper (United States Patent 6,111,301).

With respect to independent claim 1, Stamper discloses a semiconductor device formed on a substrate (see the entire reference, including the Fig. 2 disclosure, for example), comprising: an interconnection line 3/6 formed on substrate 8 and provided to structure a prescribed circuit; and a fuse 2 incorporated into said interconnection line, said fuse and a connection portion of said interconnection line electrically connected to the fuse being formed of different metals (although layers 2 and 6 are formed of the same metal (see column 2, lines 56-57), the interconnection line's layer 3 is formed of a different metal (see column 2, lines 48-56)).

Claim 1 is thus rejected under 35 U.S.C. 102(b) as being anticipated by Stamper.

Claims 1 and 9 are rejected under 35 U.S.C. 102(e) as being anticipated by Young (United States Patent 6,661,330).

With respect to independent claim 1, Young discloses a semiconductor device formed on a substrate (see the entire reference, including the Fig. 2 disclosure, for example), comprising: an interconnection line 175/135/150 formed on substrate 105 and provided to structure a prescribed circuit; and a fuse 101 incorporated into said interconnection line, said fuse and a connection portion 135/150 of said interconnection line electrically connected to the fuse being formed of different metals (see column 5, lines 30-35, and column 5, lines 52-58).

Claim 1 is thus rejected under 35 U.S.C. 102(e) as being anticipated by Young.

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With respect to dependent claim 9, Young's fuse is formed from at least two portions different in width (see the Fig. 3A-3B disclosure).

Claim 9 is thus rejected under 35 U.S.C. 102(e) as being anticipated by Young.

Claims 10 and 11 are rejected under 35 U.S.C. 102(b) as being anticipated by Delpech et al. (United States Patent 6,271,574 – hereafter Delpech).

With respect to independent claim 10, Delpech discloses a semiconductor device formed on a substrate (see the entire patent, including the Figs. 2-3 disclosure, for example), comprising: an interconnection line 14/15 formed on substrate 13 and provided to structure a prescribed circuit; and a fuse 1 incorporated into said interconnection line, said fuse having a width gradually reduced from an end toward an intermediate portion of the fuse.

Claim 10 is thus rejected under 35 U.S.C. 102(b) as being anticipated by Delpech.

With respect to dependent claim 11, Delpech's fuse has at least three different widths from the end toward the intermediate portion (i.e.,  $W_1$ ,  $W_2$  and the width(s) between  $W_1$  and  $W_2$ ).

Claim 11 is thus rejected under 35 U.S.C. 102(b) as being anticipated by Delpech.

Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Stamper (United States Patent 6,111,301) together with Delpech et al. (United States Patent 6,271,574 – hereafter Delpech).

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Claim 9 depends on independent claim 1. The explanation of the above rejection of independent claim 1 under 35 U.S.C. 102(b) as being anticipated by Stamper is hereby incorporated by reference into this rejection of dependent claim 9 under 35

The difference, therefore, between claim 9 and Stamper is claim 9's fuse is formed from at least two portions different in width.

U.S.C. 103(a) as being unpatentable over Stamper together with Delpech.

Delpech teaches that forming a fuse from at least two portions different in width increases its efficiency (see the entire patent, particularly the Fig. 3 disclosure).

It would have been obvious to one skilled in this art to form Stamper's fuse from at least two portions different in width in order to increase its efficiency, as taught by Delpech.

Claim 9 is thus rejected under 35 U.S.C. 103(a) as being unpatentable over Stamper together with Delpech.

Claims 2-8 are objected to as being dependent upon a rejected base claim, but would be allowable over the prior art of record if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art of record does not disclose or suggest the allowable semiconductor device taken as a whole, including the different metals.

Registered practitioners can telephone the examiner at (571) 272-1843. Any voicemail message left for the examiner must include the name and registration number of the registered practitioner calling, and the Application/Control (Serial) Number. Technology Center 2800's general telephone number is (571) 272-2800.

Mark V. Prenty Primary Examiner